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Sheet 1 of 1

Complete if Known	
Application Number	10/684,102
Filing Date	October 9, 2003
First Named Inventor	Chandan Mathur
Art Unit	2185
Examiner Name	
Attorney Docket Number	1934-11-3

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
VV		BAKSHI S; GAJSKI D D; "Partitioning and Pipelining for Performance-Constrained Hardware/Software Systems", IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 7, NR. 4, PG. 419-432, (1999-12-00), XP000869229	

Examiner Signature		Date Considered	4-23-07

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